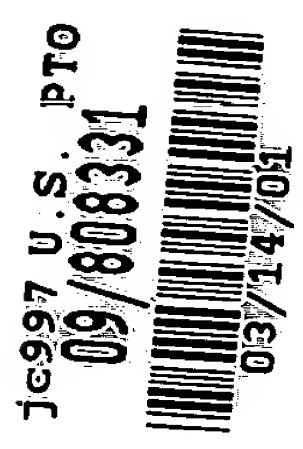


#4 IDS
M. Brunson
8/21/01

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Applicant: Philip J. Lin)
Serial No.: Not assigned) Group Art Unit: Not Assigned
Filed: March 14, 2001)
For: Signal Interconnect Incorporating)
Multiple Modular Units)
Examiner: Not Assigned)



INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

Attached Form PTO/SB/08A lists documents which may be considered to be material to the above-identified application by the Patent Examiner. Copies of the references are enclosed. Entry into the record is respectfully requested.

Respectfully submitted

By Paul M. Vargo
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CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service with sufficient postage at First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 on March 14, 2001

Rosemary K. Leber